

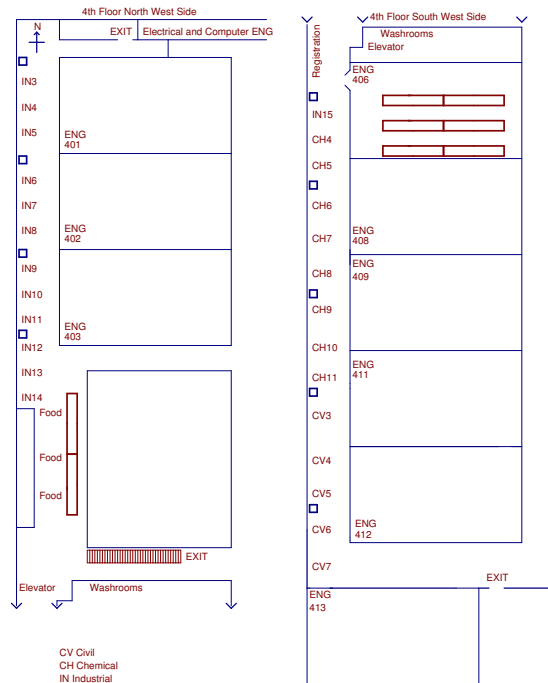
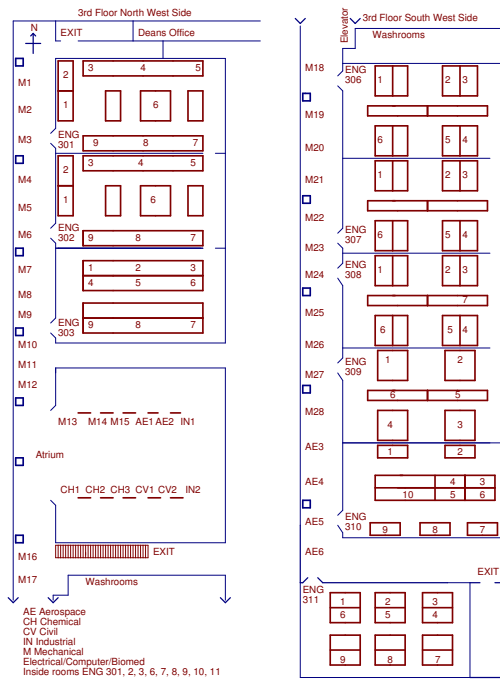
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- 4 • IMPLEMENTATION OF 4X4 ARRAY MULTIPLIER USING CADENCE VIRTUOSO TOOL IN 180NM • *F. Marium, N. Kaur, A. Gill* (M) Dr. Ye
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- 10 • DC TO AC INVERTER USING PWM • *S. Sidhu* (P) Dr. Yazdani

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ENG 3RD & 4TH FLOOR MAPS

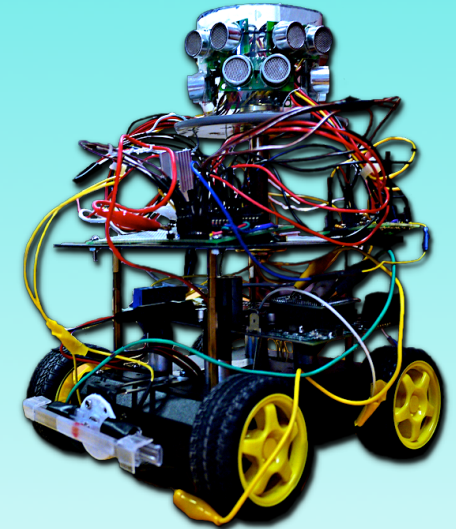


RYERSON UNIVERSITY

DEPARTMENT OF ELECTRICAL AND COMPUTER ENGINEERING

RYERSON ENGINEERING DAY • 2014

1:00 – 4:00PM • APRIL 28TH, 2014
3RD FLOOR LABS • 245 CHURCH ST.
ELECTRICAL, COMPUTER & BIOMEDICAL
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THE CAPSTONE DESIGN PROJECTS ARE LISTED BY ROOM NUMBER AND INCLUDE THE BENCH NUMBER, PROJECT TITLE, STUDENT NAMES, PROJECT CATEGORY AND THE FACULTY SUPERVISOR.

PLEASE CONSULT THE FLOORPLAN, FOUND ON THE BACK OF THIS GUIDE, TO LOCATE A SPECIFIC BENCH IN THE ROOM.

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(P) POWER (R) ROBOTICS (S) COMPUTERS/SOFTWARE

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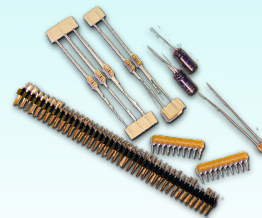
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